



### Power Field Effect Transistor

#### **GENERAL DESCRIPTION**

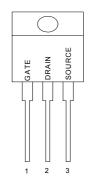
This high voltage MOSFET uses an advanced termination scheme to provide enhanced voltage-blocking capability without degrading performance over time. In addition, this advanced MOSFET is designed to withstand high energy in avalanche and commutation modes. The new energy efficient design also offers a drain-to-source diode with a fast recovery time. Designed for high voltage, high speed switching applications in power supplies, converters and PWM motor controls, these devices are particularly well suited for bridge circuits where diode speed and commutating safe operating areas are critical and offer additional and safety margin against unexpected voltage transients.

#### **FEATURES**

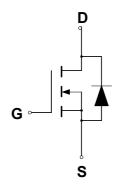
- Robust High Voltage Termination
- ◆ Avalanche Energy Specified
- Source-to-Drain Diode Recovery Time Comparable to a Discrete Fast Recovery Diode
- Diode is Characterized for Use in Bridge Circuits
- ▶ I<sub>DSS</sub> and V<sub>DS</sub>(on) Specified at Elevated Temperature
- ♦ Isolated Mounting Hole Reduces Mounting Hardware

#### PIN CONFIGURATION

TO220/TO-220F Top View



### **SYMBOL**



N-Channel MOSFET

#### **ABSOLUTE MAXIMUM RATINGS**

Rating	Symbol	Value	Unit
Drain to Current — Continuous	$I_D$	15.5	Α
- Pulsed	I <sub>DM</sub>	46.5	
Gate-to-Source Voltage — Continue	$V_{GS}$	±30	V
Total Power Dissipation – TO220	P <sub>D</sub>	197	W
– TO220FP		49	W/°C
Derate above 25℃ - TO220		1.57	
– TO220FP		0.39	
Operating and Storage Temperature Range	T <sub>J</sub> , T <sub>STG</sub>	-55 to 150	$^{\circ}\mathbb{C}$
Single Pulse Drain-to-Source Avalanche Energy $-T_J = 25^{\circ}$ C ( $V_{DD} = 100V$ , $V_{GS} = 10V$ , $I_L = 14A$ , $L = 10mH$ , $R_G = 25\Omega$ )	E <sub>AS</sub>	980	mJ
Thermal Resistance — Junction to Case -TO220	$\theta_{JC}$	0.63	°CW
<ul> <li>Junction to Case -TO220FP</li> </ul>		3.5	
<ul> <li>Junction to Ambient -TO220, TO220FP</li> </ul>	$\theta_{JA}$	62.5	
Maximum Lead Temperature for Soldering Purposes, 1/8" from case for 10 seconds	TL	260	$^{\circ}$ C
ESD SENSITIVITY — HBM, C=100pF, R=1.5kΩ	Vesd	2000	V

<sup>(1)</sup> Pulse Width and frequency is limited by TJ(max) and thermal response





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### **ORDERING INFORMATION**

Part Number	Package
GPT16N50GN220*	TO-220
GPT16N50DGN220FP*	TO-220F

<sup>\*</sup>Note: G : Suffix for PB Free Product

### **ELECTRICAL CHARACTERISTICS**

Unless otherwise specified,  $T_J = 25^{\circ}C$ .

			GPT16N50			
Characteristic		Symbol	Min	Тур	Max	Units
Drain-Source Breakdown Voltage		$V_{(BR)DSS}$	500			V
$(V_{GS} = 0 \text{ V}, I_D = 250 \ \mu \text{ A})$						
Drain-Source Leakage Current		I <sub>DSS</sub>			1	uA
$(V_{DS} = 500 \text{ V}, V_{GS} = 0 \text{ V})$						
Gate-Source Leakage Current-Forward		I <sub>GSSF</sub>			100	nA
$(V_{gsf} = 30 \text{ V}, V_{DS} = 0 \text{ V})$						
Gate-Source Leakage Current-Reverse		$I_{GSSR}$			100	nA
$(V_{gsr} = 30 \text{ V}, V_{DS} = 0 \text{ V})$						
Gate Threshold Voltage		$V_{GS(th)}$	3		5	V
$(V_{DS} = V_{GS}, I_D = 250 \ \mu A)$						
Static Drain-Source On-Resistance (V <sub>GS</sub> = 10 V, I <sub>D</sub> = 8A) *		R <sub>DS(on)</sub>			0.38	Ω
Forward Transconductance (V <sub>DS</sub> = 15 V, I <sub>D</sub> = 8A) *		<b>g</b> <sub>FS</sub>		15		S
Input Capacitance	$(V_{DS} = 25 \text{ V}, V_{GS} = 0 \text{ V},$	$C_{iss}$		2299		pF
Output Capacitance	$(v_{DS} = 25 \text{ V}, v_{GS} = 0 \text{ V},$ f = 1.0  MHz)	C <sub>oss</sub>		241		pF
Reverse Transfer Capacitance		C <sub>rss</sub>		16		pF
Turn-On Delay Time	$(V_{DD} = 250 \text{ V}, I_{D} = 16 \text{ A},$ $R_{G} = 25\Omega) *$	$t_{d(on)}$		35		ns
Rise Time		t <sub>r</sub>		65		ns
Turn-Off Delay Time		$t_{d(off)}$		84		ns
Fall Time		t <sub>f</sub>		39. 47		ns
Total Gate Charge	$(V_{DS} = 400 \text{ V}, I_{D} = 16 \text{ A},$ $V_{GS} = 10 \text{ V})^*$	$Q_g$		49.3		nC
Gate-Source Charge		$Q_{gs}$		10.18		nC
Gate-Drain Charge		$Q_{gd}$		17.5		nC
SOURCE-DRAIN DIODE CHARACTE	RISTICS					
Forward On-Voltage(1)	(I <sub>S</sub> = 16 A,	$V_{SD}$			1.5	V
Forward Turn-On Time		t <sub>on</sub>		**		ns
Reverse Recovery Time	$d_{IS}/d_t = 100A/\mu s$ )	t <sub>rr</sub>		475		ns

<sup>\*</sup> Pulse Test: Pulse Width ≤300µs, Duty Cycle ≤2%

<sup>\*\*</sup> Negligible, Dominated by circuit inductance



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### TYPICAL ELECTRICAL CHARACTERISTICS

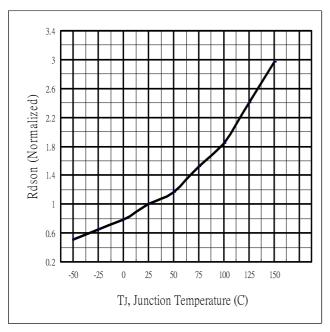


Fig 1. On-Resistance Variation with vs. Temperature

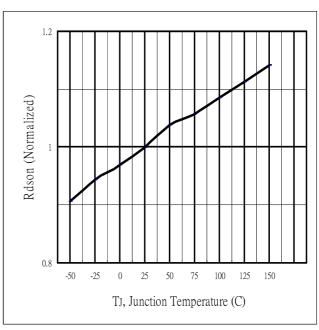


Fig.2 Breakdown Voltage Variation vs. Temperature

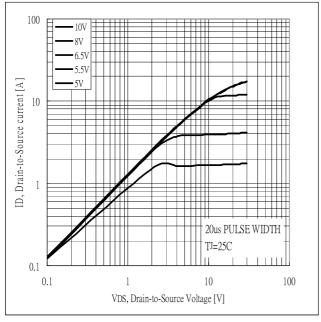


Fig 3. Typical Output Characteristics

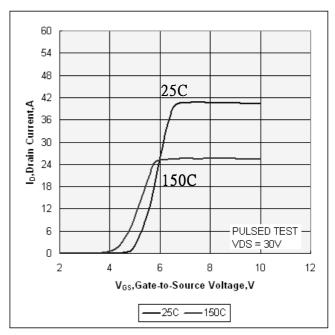


Fig 4. Typical Transfer Characteristics





# POWER FIELD EFFECT TRANSISTOR

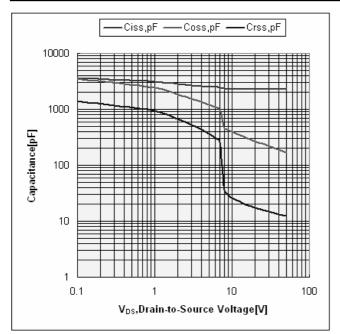


Fig 5. Typical Capacitance Vs. Drain-to-Source Voltage

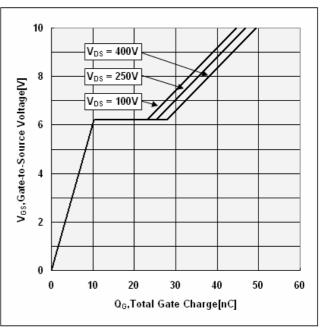


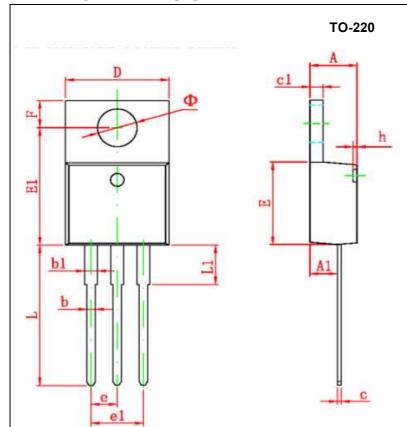
Fig 6. Typical Gate Charge Vs. Gate-to-Source Voltage





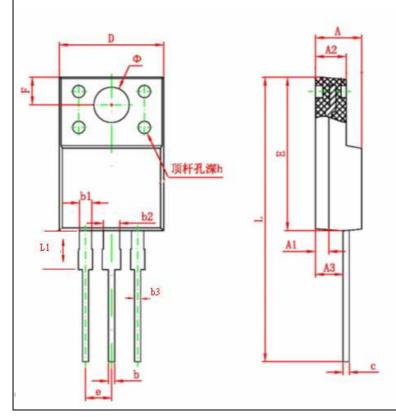
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### **PACKAGE DIMENSION**



Count al	Dimensions In Millimeters		
Symbol	Min.	Max	
Α	4.40	4.80	
A1	2.10	2.84	
Ъ	0.71	0.91	
b1	1.17	1.37	
С	0.30	0.60	
c1	1.17	1.47	
D	9.40	10.60	
Е	8.40	9.60	
е	2.54	TYP.	
e1	4.90	5.60	
F	3.00	REF.	
Φ	3.50 REF.		
h	0.00	0.30	
L	12.50	14.00	
L1	3.50	4.00	

#### TO-220F



Come le a l	Dimensions In Millimeter		
Symbol	Min.	Max	
Α	3.80	4.70	
A1	1.3	REF.	
A2	2.20	3.20	
A3	2.10	3.20	
b	0.30	0.95	
b1	1.00	1.75	
b2	1.00	1.75	
b3	0.50	0.80	
С	0.30	0.90	
D	9.90	10.40	
Е	14.60	16.20	
е	2.54 TYP.		
F	3.00 REF.		
Φ	3.50 REF.		
h	0.00	0.30	
L	28.00	30.00	
L1	3.20	3.55	





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#### **IMPORTANT NOTICE**

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深圳市冠顺微电子股份有限公司 Shenzhen Great Power Co., Ltd Web:http://www.grtpower.com

臺灣深

新北市汐止區新台五路一段 96 號 21F

21F., No. 96, Sec. 1, Sintai 5th Rd., Sijhih City, Taipei County 22102,

Taiwan, R.O.C.

TEL: +886-2-2696 3558 FAX: +886-2-2696 3559 深圳市福田区深南大道 7002 号财富广场 A座 4V,

4V, Tower A, Fortune Plaza, No. 7002, Shennan Road, Futian District, Shenzhen City, China

PC: 518040

TEL: +86-755-83709176 FAX: +86-755-83709276